

IMPORTANT PRODUCT INFORMATION

READ THIS INFORMATION FIRST

Product:

Micro PLC CPU Firmware Release 3.10

14-Point	8 DC In/6 Relay Out, AC Power	IC693UDR001RP1/CE693UDR001RP1 or later
	8 VDC In/6 Relay Out, DC Power	IC693UDR002RP1/CE693UDR002RP1 or later
	8 VAC In/6 VAC Out, AC Power	IC693UAA003RP1/CE693UAA003RP1 or later
28-Point	16 DC In/1 DC and 11 Relay Out, AC Power	IC693UDR005RP1/CE693UDR005RP1 or later
	16 AC In/12 AC Out, AC Power	IC693UAA007RP1/CE693UAA007RP1 or later
	16 DC In/1 DC and 11 Relay Out, DC Power	IC693UDR010RP1/CE693UDR010RP1 or later
23-Point	13 DC inputs, 1 DC output, 9 relay outputs, 2 analog in, 1 analog out, AC power	IC693UAL006RP1/CE693UAL006RP1 or later

Section 1 – Overview

Compatibility

Version 8.00 or later of the configuration/programming software must be used to take advantage of the Release 3 features.

The SNPX Master, RTU, and expansion unit features require the following versions or later of Micro PLC hardware:

IC693UDR001LP1	IC693UAL006BP1
IC693UDR002LP1	IC693UAA007FP1
IC693UAA003JP1	IC693UDR010BP1
IC693UDR005JP1	

New Features

RTU Slave Protocol on both the 14 and 28 point units (Port 2 only for 28 point units).

This feature is implemented, as specified in GFK-0582B. RTU is now supported in both 2-wire and 4-wire implementation. The following commands are supported.

- Read Output Table
- Read Input Table
- Read Registers

- Read Analog Input
- Force (Write) Single Output
- Preset Single Register
- Read Exception Status
- Loopback Maintenance
- Force (Write) Multiple Outputs
- Preset Multiple Registers
- Report Device Type
- Read Scratch Pad Memory

Documentation

The documentation for all Micro PLC models consists of:

IPI	GFK-1094R
User's Manual	GFK-1065E or later
Software User's Manual	GFK-0466J or later
Datasheets	GFK-1087E or later (14point) GFK-1222A or later (28point) GFK-1459 or later (23-point/analog)

Documentation is also available on CDROM (catalog number IC690CDR002L or later).

Section 2 – Special Operational Notes

Analog Potentiometers

The analog value from the potentiometer is not reported until the number of sweeps determined by the value in AQ1 has occurred. If you want to receive a value from the potentiometer on every sweep, including the first sweep, AQ1 must contain 0.

Section 3A – Problems Resolved by Release 3.00 Upgrade

Subject	Description
Date Display	On 28-point units, in non-leap years, January 1 would be displayed as December 32. The date will then roll over to January 2 one day later, correcting itself. This problem is corrected completely in release 3.00.
%R memory used for previous value table	On 28 point units, if transition coils were used, %R1617–%R1814 were used for the previous value table and therefore reserved. In release 3.00, these registers are now available to your application and the previous value table is stored in internal RAM.
HI reference used	The highest reference used by the PLC was calculated incorrectly when MSKCMP or DOIO function blocks were used in a program. This would cause logic not equal to be displayed after a store of the program and an incorrect highest reference used value to be displayed after a load of the program. This problem is corrected in release 3.00.
HSC output after one pulse	The type A High Speed Counter can now be used to turn on its output after one pulse.

Section 3B – Problems Resolved by Release 3.01 Upgrade

Subject	Description
Diagnostics disabled with retentive bit memory	In release 3.00, if diagnostics are disabled using the configuration parameter, Pwr Up Diag, bit references (%I, %Q, %M, %G) that are retentive retain their values from the last power-down with diagnostics enabled instead of from the most recent power-down. In release 3.01, retentive bit references retain their values from the most recent power-down.

Section 3C – Problems Resolved by Release 3.10 Upgrade

Subject	Description
Pulse Train Output (PTO) Doubling the Number of Counts	In release 3.00, if a Pulse Train Output (PTO) was enabled without a one-shot, for example, with an always-on contact, then the PTO could send twice the expected number of counts. This problem is corrected in release 3.10.
High Speed Counter (HSC) Pre-load Not Occurring	In release 3.00, if the pre-load is set equal to the low limit of the HSC, then the pre-load may not occur. This problem is corrected in release 3.10.