

PTC thermistors for overcurrent protection

Leaded disks, coated, 110 V

Series/Type: B598*0 Date: March 2006

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Leaded disks, coated, 110 V

C830 ... C890

Applications

Overcurrent and short-circuit protection

Features

- Lead-free terminals
- Manufacturer's logo and type designation stamped on in yellow
- UL approval to UL 1434 with V_{max} = 125 V and V_R = 110 V (file number E69802)
- VDE approval (license number 104843 E)
- RoHS-compatible

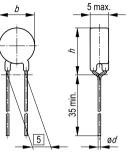
Options

- Leadless disks and leaded disks without coating available on request
- Thermistors with diameter b ≤ 11.0 mm are also available on tape (to IEC 60286-2)

Delivery mode

- Cardboard strips (standard)
- Cardboard tape reeled or in Ammo pack on request

Dimensional drawing



TPT0648-4

Dimensions (mm)

| Туре | b _{max} | h _{max} | Ød |
|------|------------------|------------------|-----|
| C830 | 22.0 | 25.5 | 0.6 |
| C840 | 17.5 | 21.0 | 0.6 |
| C850 | 13.5 | 17.0 | 0.6 |
| C860 | 11.0 | 14.5 | 0.6 |
| C870 | 9.0 | 12.5 | 0.6 |
| C880 | 6.5 | 10.0 | 0.6 |
| C890 | 4.0 | 7.5 | 0.5 |

General technical data

| Max. operating voltage | (T _A = 60 °C) | V _{max} | 160 | VDC or VAC |
|-----------------------------|--------------------------|------------------|----------|------------|
| Rated voltage | | V _R | 110 | VDC or VAC |
| Switching cycles | | Ν | 100 | |
| Reference temperature | (typ.) | T _{ref} | 160 | °C |
| Tolerance of R _R | | ΔR_{R} | ±25 | % |
| Operating temperature range | (V = 0) | T _{op} | -40/+125 | °C |
| Operating temperature range | $(V = V_{max})$ | T _{op} | 0/+60 | °C |



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Electrical specifications and ordering codes

| Туре | I _R | Is | I _{Smax} | l _r | R _R | R _{min} | Ordering code |
|------|----------------|------|-------------------|-----------------|----------------|------------------|-----------------|
| | | | $(V = V_{max})$ | $(V = V_{max})$ | | | |
| | | | | typ. | | | |
| | mA | mA | A | mA | Ω | Ω | |
| C830 | 525 | 1050 | 7.0 | 24 | 3.7 | 2.2 | B59830C0160A070 |
| C840 | 400 | 800 | 4.1 | 18 | 6 | 3.6 | B59840C0160A070 |
| C850 | 250 | 500 | 2.2 | 16 | 10 | 6.0 | B59850C0160A070 |
| C860 | 180 | 360 | 1.5 | 13 | 15 | 7.8 | B59860C0160A070 |
| C870 | 125 | 250 | 1.0 | 11 | 25 | 13.1 | B59870C0160A070 |
| C880 | 70 | 140 | 0.4 | 8 | 70 | 36.7 | B59880C0160A070 |
| C890 | 35 | 70 | 0.2 | 6 | 150 | 78.7 | B59890C0160A070 |



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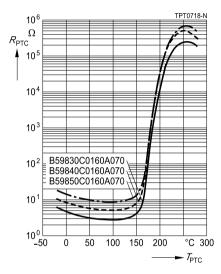
Reliability data

| Test | Standard | Test conditions | $ \Delta R_{25}/R_{25} $ | |
|-----------------------|-------------|-------------------------------------------------------|--------------------------|--|
| Electrical endurance, | IEC 60738-1 | Room temperature, I _{Smax;} V _{max} | < 25% | |
| cycling | | Number of cycles: 100 | | |
| Electrical endurance, | IEC 60738-1 | Storage at V _{max} /T _{op} | < 25% | |
| constant | | Test duration : 1000 h | | |
| Damp heat | IEC 60738-1 | Temperature of air: 40 °C | < 10% | |
| | | Relative humidity of air: 93% | | |
| | | Duration: 56 days | | |
| | | Test according to IEC 60068-2-78 | | |
| Rapid change | IEC 60738-1 | $T = T_{LCT}, T = T_{UCT}$ | < 10% | |
| of temperature | | Number of cycles: 5 | | |
| | | Test duration: 30 min | | |
| | | Test according to IEC 60068-2-14, Test Na | | |
| Vibration | IEC 60738-1 | Frequency range: 10 to 55 Hz | < 5% | |
| | | Displacement amplitude: 0.75 mm | | |
| | | Test duration: 3 · 2 h | | |
| | | Test according to IEC 60028-2-6, Test Fc | | |
| Bump | IEC 60738-1 | Pulse shape: half-sine | < 5% | |
| | | Acceleration: 50 g | | |
| | | Pulse duration: 1 ms; 6 · 3 pulses | | |
| | | Test according to IEC 60068-2-29 | | |
| Climatic sequence | IEC 60738-1 | Dry heat: T = T _{UCT} | < 10% | |
| | | Test duration: 16 h | | |
| | | Damp heat first cycle | | |
| | | Cold: $T = T_{LCT}$ | | |
| | | Test duration: 2 h | | |
| | | Damp heat 5 cycles | | |
| | | Tests performed according to | | |
| | | IEC 60068-2-30 | | |

Overcurrent protection Leaded disks, coated, 110 V

Characteristics (typical)

PTC resistance $R_{\mbox{\tiny PTC}}$ versus PTC temperature $T_{\mbox{\tiny PTC}}$ (measured at low signal voltage)

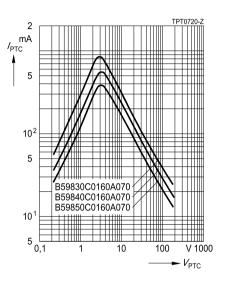


Switching time $t_{\rm S}$ versus switching current $I_{\rm S}$ (measured at 25 °C in still air)

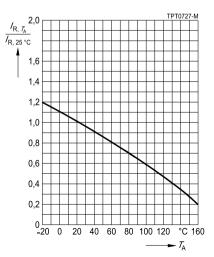
TPT0719-W 10³ s ts 10² B59830C0160A070 B59840C0160A070 B59850C0160A070 10¹ 10⁰ 10⁻¹ 0 4 8 12 16 A 20 ►/s

PTC current I_{PTC} versus PTC voltage V_{PTC} (measured at 25 °C in still air)

EPCOS



Rated current I_{R} versus ambient temperature T_{A} (measured in still air)





Characteristics (typical) PTC resistance R_{PTC} versus

PTC temperature T_{PTC}

 10^{7}

Ω

10⁶

10⁵

10⁴

10³

 10^{2}

10¹

, -50 0

R_{PTC}

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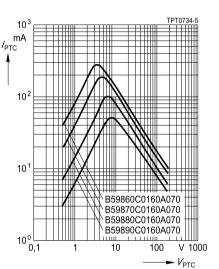
(measured at low signal voltage)

B59860C0160A070 B59870C0160A070

B59880C0160A070 B59890C0160A070

> 50 100

PTC current I_{PTC} versus PTC voltage V_{PTC} (measured at 25 °C in still air)

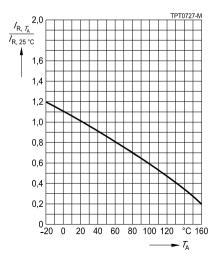


Switching time t_s versus switching current I_s (measured at 25 °C in still air)

150 200

10³ s $t_{\rm S}$ 10² B59860C0160A070 B59870C0160A070 B59880C0160A070 10¹ B59890C0160A070 10⁰ 10⁻¹ 0 2 3 A 1 4 -/s

Rated current I_R versus ambient temperature T_A (measured in still air)





TPT0732-N

°C 300

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 $- T_{PTC}$

TPT0733-W



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Cautions and warnings

General

- EPCOS thermistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- Ensure suitability of thermistor through reliability testing during the design-in phase. The thermistors should be evaluated taking into consideration worst-case conditions.

Storage

- Store thermistors only in original packaging. Do not open the package before storage.
- Storage conditions in original packaging: storage temperature -25 °C ... +45 °C, relative humidity ≤75% annual mean, maximum 95%, dew precipitation is inadmissible.
- Avoid contamination of thermistors surface during storage, handling and processing.
- Avoid storage of thermistor in harmful environment with effect on function on long-term operation (examples given under operation precautions).
- Use thermistor within 6 months after delivery.

Handling

- PTCs must not be dropped. Chip-offs must not be caused during handling of PTCs.
- Components must not be touched with bare hands. Gloves are recommended.
- Avoid contamination of thermistor surface during handling.

Soldering

- Use rosin-type flux or non-activated flux.
- Insufficient preheating may cause ceramic cracks.
- Rapid cooling by dipping in solvent is not recommended.
- Complete removal of flux is recommended.

Mounting

- Electrode must not be scratched before/during/after the mounting process.
- Contacts and housing used for assembly with thermistor have to be clean before mounting. Especially grease or oil must be removed.
- When PTC thermistors are encapsulated with sealing material, the precautions given in chapter "Mounting instructions", "Sealing and potting" must be observed.
- When the thermistor is mounted, there must not be any foreign body between the electrode of the thermistor and the clamping contact.
- The minimum force of the clamping contacts pressing against the PTC must be 10 N.
- During operation, the thermistor's surface temperature can be very high. Ensure that adjacent components are placed at a sufficient distance from the thermistor to allow for proper cooling at the thermistors.
- Ensure that adjacent materials are designed for operation at temperatures comparable to the surface temperature of thermistor. Be sure that surrounding parts and materials can withstand this temperature.
- Avoid contamination of thermistor surface during processing.



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Operation

- Use thermistors only within the specified temperature operating range.
- Use thermistors only within the specified voltage and current ranges.
- Environmental conditions must not harm the thermistors. Use thermistors only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions.Contact with any liquids and solvents should be prevented.
- Be sure to provide an appropriate fail-safe function to prevent secondary product damage caused by abnormal function (e.g. use VDR for limitation of overvoltage condition).

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